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Application Number	09/938,104
Filing Date	August 23, 2001
First Named Inventor	Adam Strauss
Group Art Unit	2641
Examiner Name	Unassigned
Attorney Docket Number	42390P1253

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OTHER ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JW		TEXAS INSTRUMENTS, DTMF Tone Generation and Detection An Implementation Using the TMS320C54x Application Report, SPRA096, June 1997	
JW		J. CAVANAGH, Digital Computer Arithmetic, Design and Implementation, McGraw-Hill Book Company, 1994, pgs. 1-12.	
JW		R. KAIN, Advanced Computer Architecture, A Systems Design Approach, Department of Electrical Engineering, University of Minnesota, Prentice Hall, NJ, pgs. 16-17.	
JW		J. MICK & J. BRICK, Bit-Slice Microprocessor Design, McGraw-Hill Book Company, 1990, pg. 191	
JW		D. PATTERSON & J. HENNESSY, Computer Architecture A Quantitative Approach, Morgan Kaufmann Publishers, Inc., San Mateo, Ca 1990, pgs 142-143.	
JW		R65C00/21 Dual CMOS Microcomputer and R65C29 Dual CMOS Microprocessor; Rockwell 1984 Data Book; Rockwell International; Doc. No. 29651N64; pgs. 3-1 - 3-33.	
JW		J. McCORMICK, Supporting Predicated Execution: Techniques and Tradeoffs, www.crhc.uiuc.edu/IMPACT/abstracts/report/ms-thesis-jim-mccormick.html, 10/15/99	
JW		Instructions Set Architecture, Web based course, www.-ee.eng.hawaii.edu/~tep/EE461/Notes/ISA/isa.html, 10/15/99	
JW		W. STALLINGS, Computer Organization and Architecture 4th Edition, pgs. 313-386, Prentice-Hall, Inc. New Jersey, 1993	
JW		Texas Instruments, SMJ320C80 Digital Signal Processor Data Sheet; document No. SGUS025; August 1998; Texas Instruments	
JW		MINOLI & MINOLI; Chapter 5, Technology and Standards for Low-Bit-Rate Vocoding Methods; Delivering Voice Over IP Networks; 1998; pgs. 149-233; Robert Ipsen Pub.	
JW		R. FROMM, Instruction Set Architecture Simulation Infrastructure ... Development, www.ilpsoft.eecs.berkely.edu:9636/~9636/~ilpsoft/99abstracts/rfromm.1.html, 10/15/99	
JW		M. FELDER, J. MASON & B. EVANS; Effieicent Dual-tone Multifrequency Detection Using the Nonuniform Discrete Fourier Transform, IEEE Signal Processing Letters, 6/98	

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